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## DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Semiconductor Processing Methods of Forming a Conductive Projection and Methods of Increasing Alignment Tolerances, Serial No. 09/032,254, filed 02/27/98.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

## PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under

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Section 1001 of Title 18 of the United States Code and that such willful false statement may jeopardize the validity of the application or any patent issued therefrom.

3	patent issued therefrom	•
4		* * * * * * * * *
5	Full name of inventor:	Mark Fischer
6	Inventor's Signature:	Mark Fischer
7	Date: -7/17/98	<u> </u>
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2	Full name of inventor:	John K. Zahurak
3	Inventor's Signature:	for X. John
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